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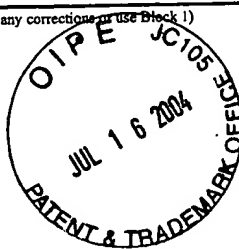
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04/13/2004

James H Morris
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GAIL DRISCOLL	(Depositor's name)
<i>Gail Driscoll</i>	(Signature)
7-13-04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/625,116	07/25/2000	Isabelle Claverie	S1022/8500	7278

TITLE OF INVENTION: PROTECTION OF THE LOGIC WELL OF A COMPONENT INCLUDING AN INTEGRATED MOS POWER TRANSISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	07/13/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
BENENSON, BORIS	2836	361-056000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Lisa K. Jorgenson

2 James H. Morris

3 Wolf, Greenfield & Sacks, P.C.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

SGS-Thomson Microelectronics S.A. Gentilly, France

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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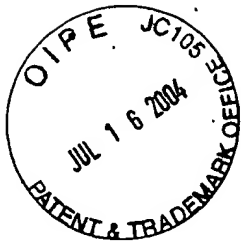
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ATTORNEY'S DOCKET NO.: S1022.80500US00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Isabelle CLAVERIE
Serial No.: 09/625,116
Filed: July 25, 2000
For: PROTECTION OF THE LOGIC WELL OF A COMPONENT INCLUDING AN
INTEGRATED MOS POWER TRANSISTOR

Examiner: Boris Benneson
Art Unit: 2836

Confirmation No.: 7278

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Attorney Docket No.: S1022.80500US00
X07/13/04

Respectfully submitted,

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